

L Number	Hits	Search Text	DB	Time stamp
1	3251	RIGHTS WITH MANAGEMENT	USPAT; US-PGPUB; EPO; IBM_TDB	2004/03/19 08:34
2	471	(RIGHTS WITH MANAGEMENT) AND @AD<19970225	USPAT; US-PGPUB; EPO; IBM_TDB	2004/03/19 08:34
3	36	((RIGHTS WITH MANAGEMENT) AND @AD<19970225) AND (BLOCK WITH FORMAT)	USPAT; US-PGPUB; EPO; IBM_TDB	2004/03/19 08:51
4	27	((RIGHTS WITH MANAGEMENT) AND @AD<19970225) AND (RULE WITH RIGHTS)	USPAT; US-PGPUB; EPO; IBM_TDB	2004/03/19 08:52
5	16	((RIGHTS WITH MANAGEMENT) AND @AD<19970225) AND (BLOCK WITH FORMAT)) AND ((RIGHTS WITH MANAGEMENT) AND @AD<19970225) AND (RULE WITH RIGHTS))	USPAT; US-PGPUB; EPO; IBM_TDB	2004/03/19 08:53
6	11	((RIGHTS WITH MANAGEMENT) AND @AD<19970225) AND (RULE WITH RIGHTS)) NOT (((RIGHTS WITH MANAGEMENT) AND @AD<19970225) AND (BLOCK WITH FORMAT)) AND ((RIGHTS WITH MANAGEMENT) AND @AD<19970225) AND (RULE WITH RIGHTS))	USPAT; US-PGPUB; EPO; IBM_TDB	2004/03/19 08:53
7	6	(((RIGHTS WITH MANAGEMENT). AND @AD<19970225) AND (BLOCK WITH FORMAT)) AND ((RIGHTS WITH MANAGEMENT) AND @AD<19970225) AND (RULE WITH RIGHTS))) AND CRYPTOGRAPHIC	USPAT; US-PGPUB; EPO; IBM_TDB	2004/03/19 09:00
8	6	(((RIGHTS WITH MANAGEMENT) AND @AD<19970225) AND (BLOCK WITH FORMAT)) AND ((RIGHTS WITH MANAGEMENT) AND @AD<19970225) AND (RULE WITH RIGHTS))) AND ENCRYPT\$3	USPAT; US-PGPUB; EPO; IBM_TDB	2004/03/19 09:00

Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition	Errors
1	BRS	L1	RIGHTS WITH MANAGEMENT	USPA T; US-P GPUB ; EPO; IBM_ TDB_	2004/03/1 9 08:34			0
2	BRS	L2	471 1 AND @AD<19970225	USPA T; US-P GPUB ; EPO; IBM_ TDB_	2004/03/1 9 08:34			0
3	BRS	L3	2 AND (BLOCK WITH FORMAT)	USPA T; US-P GPUB ; EPO; IBM_ TDB_	2004/03/1 9 08:51			0
4	BRS	L4	2 AND (RULE WITH RIGHTS)	USPA T; US-P GPUB ; EPO; IBM_ TDB_	2004/03/1 9 08:52			0

Type	L #	Hits	Search Text	DBS	Time Stamp	Comments	Error Definition	Error Count
5 BRS	L5	16	3 AND 4	USPA T; US-P GPUB ; EPO; IBM_ TDB	2004/03/1 9 08:53			0
6 BRS	L6	11	4 NOT 5	USPA T; US-P GPUB ; EPO; IBM_ TDB	2004/03/1 9 08:53			0
7 BRS	L7	6	5 AND CRYPTOGRAPHIC	USPA T; US-P GPUB ; EPO; IBM_ TDB	2004/03/1 9 09:00			0
8 BRS	L8	6	5 AND ENCRYPT\$3	USPA T; US-P GPUB ; EPO; IBM_ TDB	2004/03/1 9 09:00			0

U	V	1	Document ID	Issue Date	Pages	Title	Current OR XRef	Current XRef	Retrieval Classif
1	<input checked="" type="checkbox"/>	US B1	6240508	20010529	42	Decode and execution synchronized pipeline processing using decode generated memory read queue with stop entry to allow execution of generated memory read	712/219	710/39; 713/502	
2	<input checked="" type="checkbox"/>	US A	6122403	20000919	110	Computer system linked by using information in data objects	382/233		
3	<input checked="" type="checkbox"/>	US A	5949876	19990907	320	Systems and methods for secure transaction management and electronic rights protection	705/80	705/1; 705/39; 705/54	
						Steganographic techniques for securely delivering electronic digital rights management control information over insecure communication channels	705/54	705/58; 705/76; 713/176	<u>997 Inv L5</u>
4	<input checked="" type="checkbox"/>	US A	5943422	19990824	68				
5	<input checked="" type="checkbox"/>	US A	5926636	19990720	17	Remote procedural call component management method for a heterogeneous computer network	719/313	709/232; 719/328; 719/330	
6	<input checked="" type="checkbox"/>	US A	5917912	19990629	320	System and methods for secure transaction management and electronic rights protection	713/187	705/40; 713/164; 719/312	
7	<input checked="" type="checkbox"/>	US A	5915019	19990622	317	Systems and methods for secure transaction management and electronic rights protection	705/54	705/26; 705/400; 713/200	

	Inventor	S	C	P	2	3	4	5	Image Doc. Displayed	PT
1	Brown, III, John F. et al.	<input type="checkbox"/>	US 6240508	<input type="checkbox"/>						
2	Rhoads, Geoffrey B.	<input type="checkbox"/>	US 6122403	<input type="checkbox"/>						
3	Ginter, Karl L. et al.	<input type="checkbox"/>	US 5949876	<input type="checkbox"/>						
4	Van Wie, David M. et al.	<input type="checkbox"/>	US 5943422	<input type="checkbox"/>						
5	Lam, Geoffrey T. et al.	<input type="checkbox"/>	US 5926636	<input type="checkbox"/>						
6	Ginter, Karl L. et al.	<input type="checkbox"/>	US 5917912	<input type="checkbox"/>						
7	Ginter, Karl L. et al.	<input type="checkbox"/>	US 5915019	<input type="checkbox"/>						

V	1	Document ID	Issue Date	Pages	Title	Current OR XRef	Current XRef	Retrieval Classif
8	<input checked="" type="checkbox"/>	US 5910987	19990608	311	Systems and methods for secure transaction management and electronic rights protection	705/52	705/30	Intercept R
9	<input checked="" type="checkbox"/>	US 5900904	19990504	29	Picture reproduction position detecting apparatus for use with video on demand system	725/92	725/115; 725/88; 725/93	
10	<input checked="" type="checkbox"/>	US 5892900	19990406	360	Systems and methods for secure transaction management and electronic rights protection	713/200	713/201	Fla 534 D 1186
11	<input checked="" type="checkbox"/>	US 5809320	19980915	56	High-performance multi-processor having floating point unit	712/34	712/42	
12	<input checked="" type="checkbox"/>	US 5784630	19980721	53	Method and apparatus for processing data in multiple modes in accordance with parallelism of program by using cache memory	712/30	709/213; 712/229	
13	<input checked="" type="checkbox"/>	US 5664100	19970902	28	Data transmission processing method and apparatus	709/232	370/230; 707/205	
14	<input checked="" type="checkbox"/>	US 5586307	19961217	25	Method and apparatus supplying synchronous clock signals to circuit components	713/400	327/144; 331/174	
15	<input checked="" type="checkbox"/>	US 5542058	19960730	42	Pipelined computer with operand context queue to simplify context-dependent execution flow	713/502	712/23	
16	<input checked="" type="checkbox"/>	US 5481689	19960102	58	Conversion of internal processor register commands to I/O space addresses	711/202	711/212	

	Inventor	S	C	P	2	3	4	5	Image Doc. Displayed	PT
8	Ginter, Karl L. et al.	<input type="checkbox"/>	US 5910987	<input type="checkbox"/>						
9	Okada, Akihiro et al.	<input type="checkbox"/>	US 5900904	<input type="checkbox"/>						
10	Ginter, Karl L. et al.	<input type="checkbox"/>	US 5892900	<input type="checkbox"/>						
11	Jain, Anil et al.	<input type="checkbox"/>	US 5809320	<input type="checkbox"/>						
12	Saito, Massahiko et al.	<input type="checkbox"/>	US 5784630	<input type="checkbox"/>						
13	Miura, Iwao	<input type="checkbox"/>	US 5664100	<input type="checkbox"/>						
14	Wong , Keng L. et al.	<input type="checkbox"/>	US 5586307	<input type="checkbox"/>						
15	Brown, III, John E. et al.	<input type="checkbox"/>	US 5542058	<input type="checkbox"/>						
16	Stamm, Rebecca L. et al.	<input type="checkbox"/>	US 5481689	<input type="checkbox"/>						

	<input checked="" type="checkbox"/>	1	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval Classif
17	<input checked="" type="checkbox"/>	<input type="checkbox"/> A	US 5394529	19950228	58	Branch prediction unit for high-performance processor	712/240		
18	<input checked="" type="checkbox"/>	<input type="checkbox"/> A	US 5379153	19950103	42	Recording and reproducing apparatus having program information	360/27	360/31; 360/61; 360/62; 360/69; 360/74 .4	
19	<input checked="" type="checkbox"/>	<input type="checkbox"/> A	US 5333296	19940726	57	Combined queue for invalidates and return data in multiprocessor system	711/171	711/117	
20	<input checked="" type="checkbox"/>	<input type="checkbox"/> A	US 5317720	19940531	59	Processor system with writeback cache using writeback and non writeback transactions stored in separate queues	711/143	711/122	
21	<input checked="" type="checkbox"/>	<input type="checkbox"/> A	US 5316341	19940531	103	Hypertext book attachment	281/15.1	281/16; 281/21.1; 281/51	
22	<input checked="" type="checkbox"/>	<input type="checkbox"/> A	US 5301192	19940405	25	Temporary information storage system comprising a buffer memory storing data structured in fixed or variable length data blocks	711/100	370/412	
23	<input checked="" type="checkbox"/>	<input type="checkbox"/> A	US 5211563	19930518	29	Computer assisted learning support system and processing method therefor	434/322		
24	<input checked="" type="checkbox"/>	<input type="checkbox"/> A	US 5204954	19930420	46	Remote storage management mechanism and method	710/22	710/5	

	Inventor	S	C	P	2	3	4	5	Image Doc. Displayed	PT
17	Brown, III, John F. et al.	<input type="checkbox"/>	US 5394529	<input type="checkbox"/>						
18	Ishii, Toshiyuki et al.	<input type="checkbox"/>	US 5379153	<input type="checkbox"/>						
19	Bouchard, Gregg et al.	<input type="checkbox"/>	US 5333296	<input type="checkbox"/>						
20	Stamm, Rebecca L. et al.	<input type="checkbox"/>	US 5317720	<input type="checkbox"/>						
21	Schwartz, David C.	<input type="checkbox"/>	US 5316341	<input type="checkbox"/>						
22	Henrion, Michel	<input type="checkbox"/>	US 5301192	<input type="checkbox"/>						
23	Haga, Hirohide et al.	<input type="checkbox"/>	US 5211563	<input type="checkbox"/>						
24	Hammer, William E. et al.	<input type="checkbox"/>	US 5204954	<input type="checkbox"/>						

U	1	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval Classif
25	<input checked="" type="checkbox"/>	US A 5175681	19921229	36	Computerized system for managing preparation and prosecution of applications in various countries for protection of industrial property rights		705/9	
26	<input checked="" type="checkbox"/>	US A 5155843	19921013	59	Error transition mode for multi-processor system	714/5		
27	<input checked="" type="checkbox"/>	US A 5142618	19920825	54	Window management apparatus for a document creating apparatus	715/516	715/530	
28	<input checked="" type="checkbox"/>	US A 5003499	19910326	64	Document preparation apparatus having rearrangement apparatus for rearranging text according to region attribute information	715/520	715/531	
29	<input checked="" type="checkbox"/>	US A 4951233	19900821	27	Document producing apparatus having in-document layout display	715/525		345/804; 715/523; 715/527; 715/530
30	<input checked="" type="checkbox"/>	US A 4939670	19900703	50	PC driven print file utilities for mainframe computer printing	358/1.11		345/441; 345/443; 715/500
31	<input checked="" type="checkbox"/>	US A 4875161	19891017	58	Scientific processor vector file organization	711/157		708/520; 711/169; 711/5
32	<input checked="" type="checkbox"/>	US A 4873630	19891010	392	Scientific processor to support a host processor referencing common memory	712/3		712/5; 712/6; 712/9
33	<input checked="" type="checkbox"/>	US A 4860007	19890822	10	Integrated primary flight display	340/973		244/1R; 340/977; 340/979; 73/179

	Inventor	S	C	P	2	3	4	5	Image Doc. Displayed	PT
25	Iwai, Takeyuki et al.	<input type="checkbox"/>	US 5175681	<input type="checkbox"/>						
26	Stamm, Rebecca L. et al.	<input type="checkbox"/>	US 5155843	<input type="checkbox"/>						
27	Fujiwara, Masaki et al.	<input type="checkbox"/>	US 5142618	<input type="checkbox"/>						
28	Fujiwara, Masaki et al.	<input type="checkbox"/>	US 5003499	<input type="checkbox"/>						
29	Fujiwara, Masaki et al.	<input type="checkbox"/>	US 4951233	<input type="checkbox"/>						
30	Freiman, Alex C. et al.	<input type="checkbox"/>	US 4939670	<input type="checkbox"/>						
31	Lahti, Archie E.	<input type="checkbox"/>	US 4875161	<input type="checkbox"/>						
32	Rusterholz, John T. et al.	<input type="checkbox"/>	US 4873630	<input type="checkbox"/>						
33	Konické, Michael L. et al.	<input type="checkbox"/>	US 4860007	<input type="checkbox"/>						

	<input checked="" type="checkbox"/>	1	Document ID	Issue Date	Pages	Title	Current OR XRef	Current XRef	Retrieval Classif
34	<input checked="" type="checkbox"/>	US 4825438 A	19890425	394	Bus error detection employing parity verification	714/56	710/100; 714/43;		
35	<input checked="" type="checkbox"/>	US 4734909 A	19880329	396	Versatile interconnection bus	370/462	714/802		
36	<input checked="" type="checkbox"/>	US 4325120 A	19820413	156	Data processing system	711/202	340/825.5	711/221	

	Inventor	S	C	P	2	3	4	5	Image Doc. Displayed	PT
34	Bennett, Donald B. et al.	<input type="checkbox"/>	US 4825438	<input type="checkbox"/>						
35	Bennett, Donald B. et al.	<input type="checkbox"/>	US 4734909	<input type="checkbox"/>						
36	Colley, Stephen R. et al.	<input type="checkbox"/>	US 4325120	<input type="checkbox"/>						

	U	1	Document ID	Issue Date	Pages	Title	Current OR XRef	Current Retrieval Classif
1	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 6272672 B1	20010807	122	Dataflow processing with events	717/107	717/108
2	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5970479 A	19991019	139	Methods and apparatus relating to the formulation and trading of risk management contracts	705/37	705/4
3	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5715403 A	19980203	43	System for controlling the distribution and use of digital works having attached usage rights where the usage rights are defined by a usage rights grammar	705/44	705/54; 705/57; 709/229; 713/202
4	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5671363 A	19970923	14	Private stock option account control and exercise system	705/37	
5	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5638443 A	19970610	42	System for controlling the distribution and use of composite digital works	705/54	
6	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5634012 A	19970527	43	System for controlling the distribution and use of digital works having a fee reporting mechanism	705/39	
(7)	<input type="checkbox"/>	<input type="checkbox"/>	US 5629980 A	19970513	44	System for controlling the distribution and use of digital works	705/54	
8	<input type="checkbox"/>	<input type="checkbox"/>	US 5291596 A	19940301	10	Data management method and system with management table indicating right of use	707/8	
9	<input type="checkbox"/>	<input type="checkbox"/>	US 5249260 A	19930928	22	Data input system	706/45	706/50; 706/907; 707/104.1
10	<input type="checkbox"/>	<input type="checkbox"/>	US 4338791 A	19820713	28	Microcomputer control for heat pump system	62/160	165/242; 236/1EA

	Inventor	S	C	P	2	3	4	5	Image Doc. Displayed	PT
1	Conway, Melvin E.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 6272672	<input type="checkbox"/>
2	Shepherd, Ian K.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 5970479	<input type="checkbox"/>
3	Stefik, Mark J.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 5715403	<input type="checkbox"/>
4	Cristofich, John M. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 5671363	<input type="checkbox"/>
5	Stefik, Mark J. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 5638443	<input type="checkbox"/>
6	Stefik, Mark J. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 5634012	<input type="checkbox"/>
7	Stefik, Mark J. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5629980	<input type="checkbox"/>					
8	Mita, Makoto	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5291596	<input type="checkbox"/>					
9	Nigawara, Seiitsu et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 5249260	<input type="checkbox"/>					
10	Stamp, Jr., Custis L. et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	US 4338791	<input type="checkbox"/>					

U	I	Document ID	Issue Date	Pages	Title	Current OR XRef	Current	Retrieval Classif
11	<input checked="" type="checkbox"/> <input type="checkbox"/> A1	WO 9810381	19980312	490	TRUSTED INFRASTRUCTURE SUPPORT SYSTEMS, METHODS AND TECHNIQUES FOR SECURE ELECTRONIC COMMERCE, ELECTRONIC TRANSACTIONS, COMMERCE PROCESS CONTROL AND AUTOMATION, DISTRIBUTED COMPUTING, AND RIGHTS MANAGEMENT			

	Inventor	S	C	P	2	3	4	5	Image Doc. Displayed	PT
11	SHEAR, VICTOR H et al.	<input type="checkbox"/>	WO 9810381 A1	<input type="checkbox"/>						